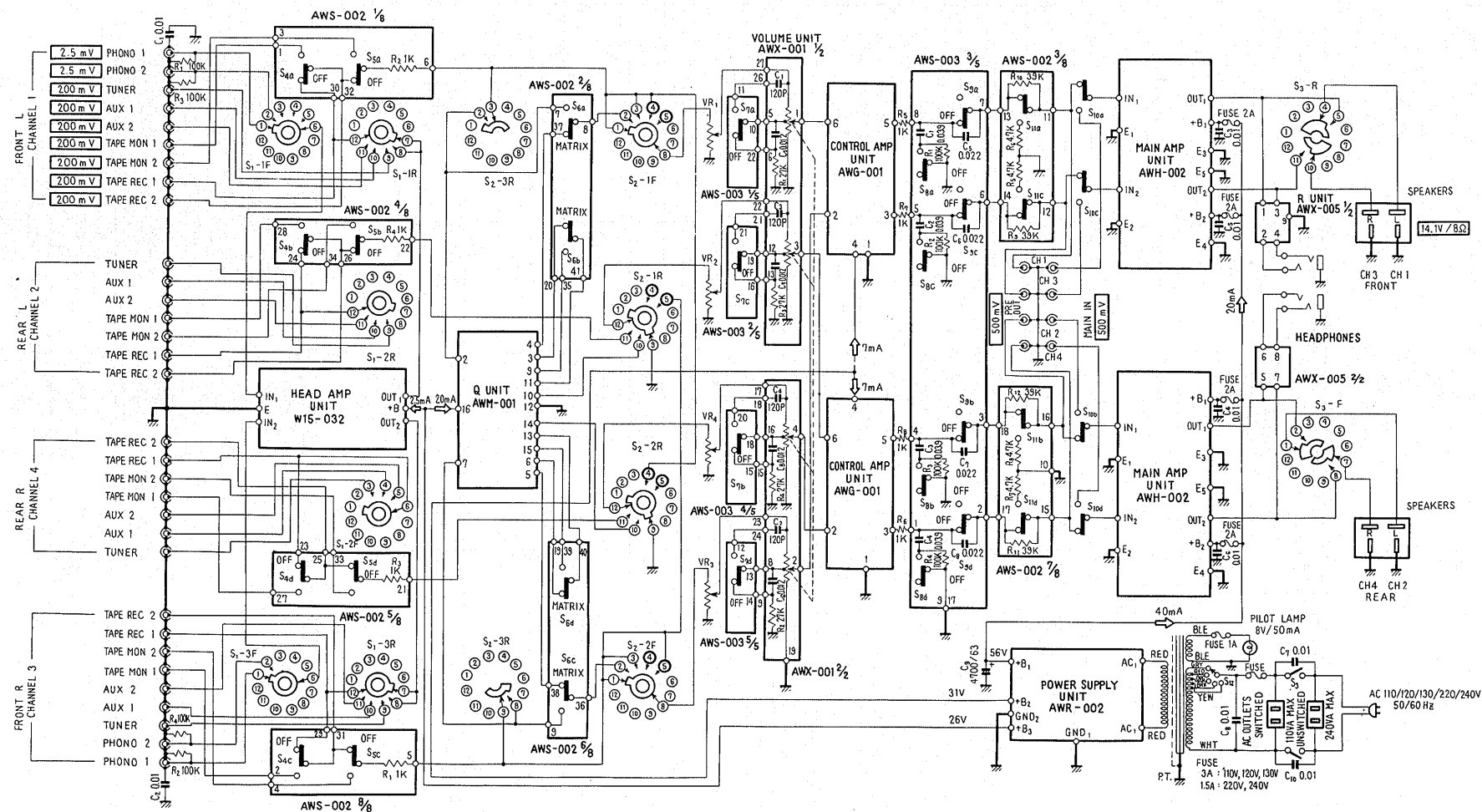


QUADRAPHONIC AMPLIFIER

QA-800

UNIT	ROUND LABEL					
	FVW					
FM FRONT END						
FM IF UNIT						
MPX UNIT						
AM TUNER UNIT						
Q UNIT	AWM-001					
HEAD AMP UNIT	W15-032					
CONTROL AMP UNIT	AWG-001					
MAIN AMP UNIT	AWH-002					
POWER SUPPLY UNIT	AWR-002					
SWITCH UNIT	AWS-002					
SWITCH UNIT	AWS-003					
SWITCH UNIT						
VR UNIT	AWX-001					
R UNIT	AWX-005					
MIC AMP UNIT						
REVERBE AMP UNIT						
REVERBE UNIT						





SWITCHES

S_1 : SELECTOR
 (1) PHONO 1
 (2) PHONO 2
 (3) TUNER
 (4) AUX 1
 (5) AUX 2

 S_2 : MODE

(1) 2CH STEREO
 (2) QUADRALIZER
 (3) 4CH STEREO
 (4) L(CH-1) MONO
 (5) R(CH-3) MONO

 S_3 : SPEAKERS

(1) POWER OFF
 (2) 4CH ST
 (3) REAR OFF
 (4) SPEAKER OFF

 S_4 :

TAPE MONITOR 1 OFF $\leftrightarrow$ ON
 S_5 : TAPE MONITOR 2 OFF $\leftrightarrow$ ON
 S_6 : QUADRALIZER
 MATRIX $\leftrightarrow$ PHASE SHIFT

S_7 : LOUDNESS OFF $\leftrightarrow$ ON
 S_8 : HIGH FILTER OFF $\leftrightarrow$ ON
 S_9 : LOW FILTER OFF $\leftrightarrow$ ON
 S_{10} : PRE & MAIN
 INTER COUPLED $\leftrightarrow$ SEPARATED
 S_{11} : MUTING OFF $\leftrightarrow$ ON

POTENTIOMETERS

VR_1 : LEVEL CONTROL (CH.1)
 VR_2 : LEVEL CONTROL (CH.2)
 VR_3 : LEVEL CONTROL (CH.3)
 VR_4 : LEVEL CONTROL (CH.4)
 VR_5 : VOLUME CONTROL

RESISTORS

NON MARK: OHM
 K: KILOHM
 M: MEGOHM

CAPACITORS

NON MARK: μ F
 P: μ MF

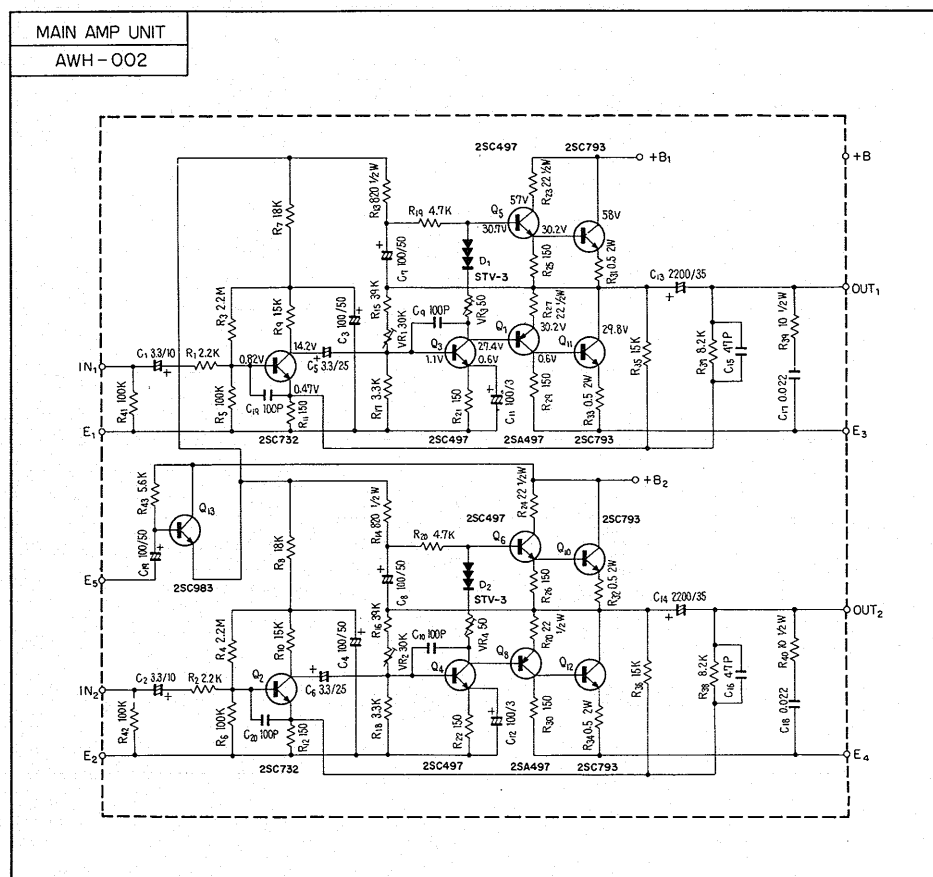
$\square$ V: SIGNAL VOLTAGE AT 25W/8 Ω OUTPUT
 V: DC VOLTAGE AT NO INPUT SIGNAL
 $\rightarrow$ DC CURRENT AT NO INPUT SIGNAL

The schematic diagram of the AWM-001 circuit is a multi-stage transistor amplifier. It features 12 transistors, all of the 2SC870 type, arranged in two rows. The top row contains transistors Q₁, Q₃, Q₇, Q₉, and Q₁₁. The bottom row contains transistors Q₂, Q₄, Q₆, Q₈, Q₁₀, and Q₁₂. Each transistor is biased using a network of resistors (R₁ through R₄₅) and capacitors (C₁ through C₂₅). The circuit is powered by a 12V supply. The output of the final stage (Q₁₂) is taken from the collector and is coupled to a common emitter load resistor R₄₅ (100Ω) and a bypass capacitor C₄ (100μF). The circuit is divided into two main sections by a dashed line, with the top section containing transistors Q₁, Q₃, Q₇, Q₉, and Q₁₁, and the bottom section containing transistors Q₂, Q₄, Q₆, Q₈, Q₁₀, and Q₁₂.

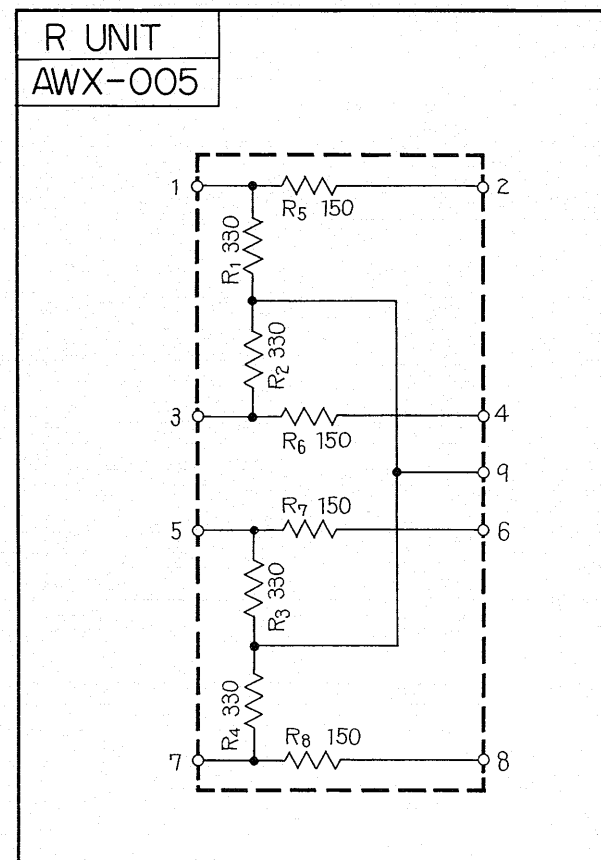
The diagram shows a two-channel audio amplifier. The top channel uses a 2SC871 (Q1) and 2SC870 (Q3) transistor pair. The bottom channel uses a 2SC871 (Q2) and 2SC870 (Q4) transistor pair. The circuit is powered by a 30V supply (+B) and ground (GND3/GND4). The input signals are IN1 and IN2. The outputs are OUT1 and OUT2. The circuit includes various resistors (R1-R24) and capacitors (C1-C16). The 2SC871 transistors are configured as common-emitter amplifiers, and the 2SC870 transistors are configured as common-emitter amplifiers. The circuit also includes a feedback network and a tone control network.

[illegible]

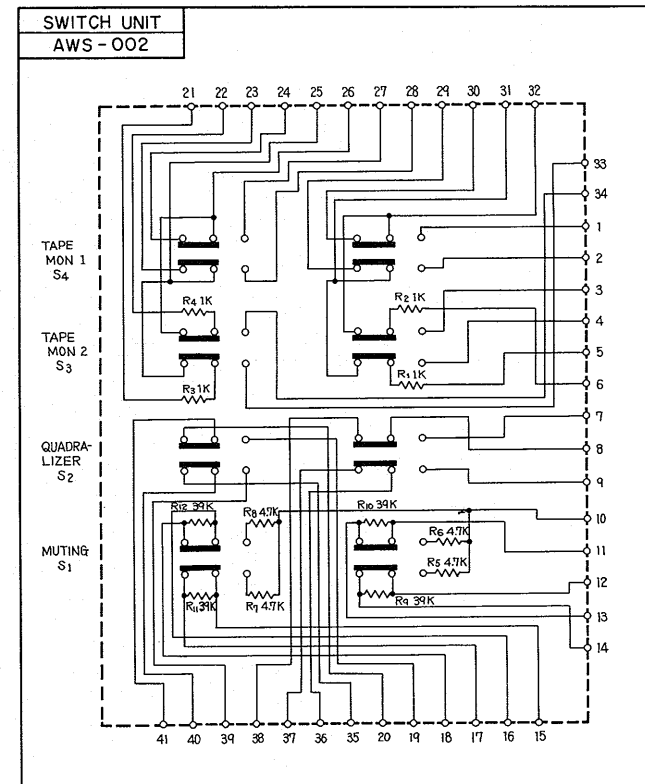
MAIN AMP UNIT



R UNIT



SWITCH UNIT

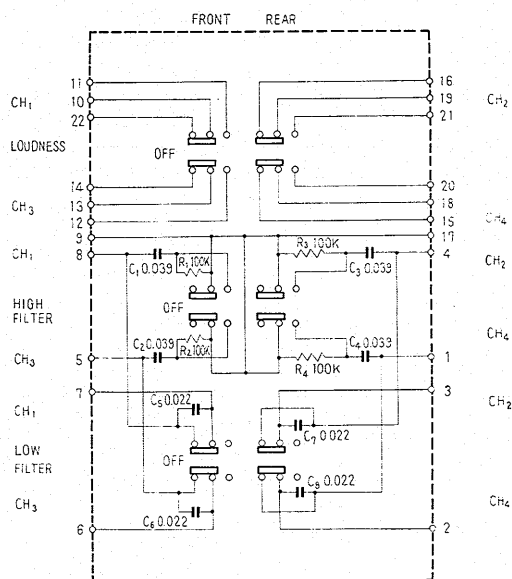


SWITCH UNIT

VR UNIT

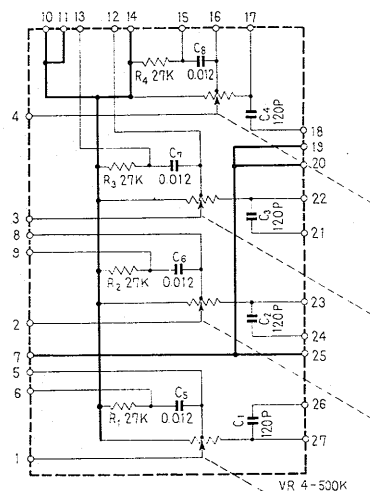
SWITCH UNIT

AWS - 003



VR UNIT

AWX - 001



POWER SUPPLY UNIT

POWER SUPPLY UNIT

AWR - 002

