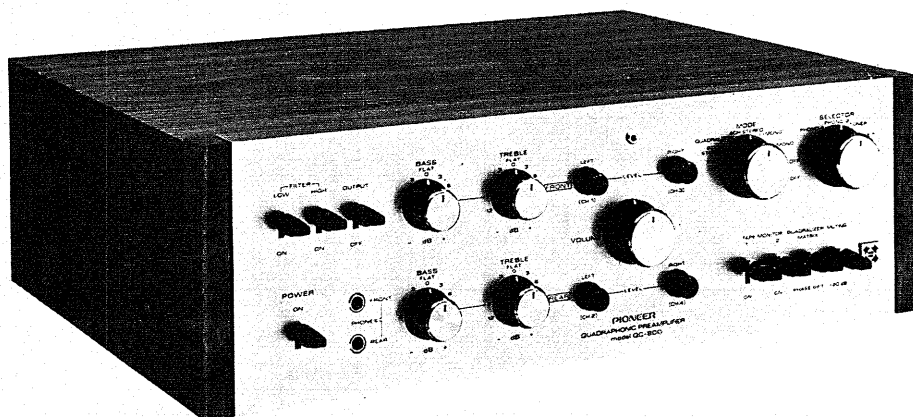
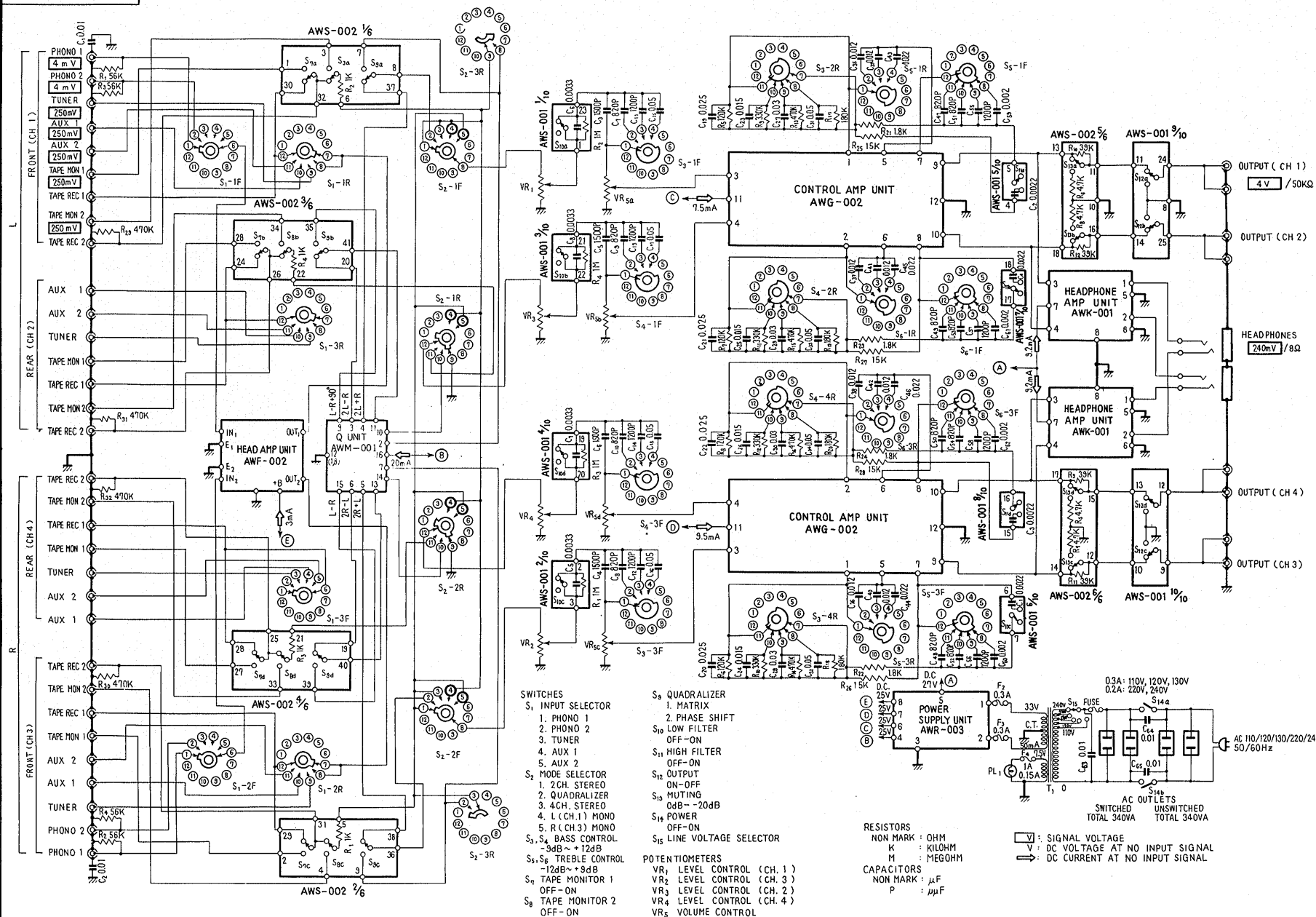


QUADRAPHONIC PREAMPLIFIER

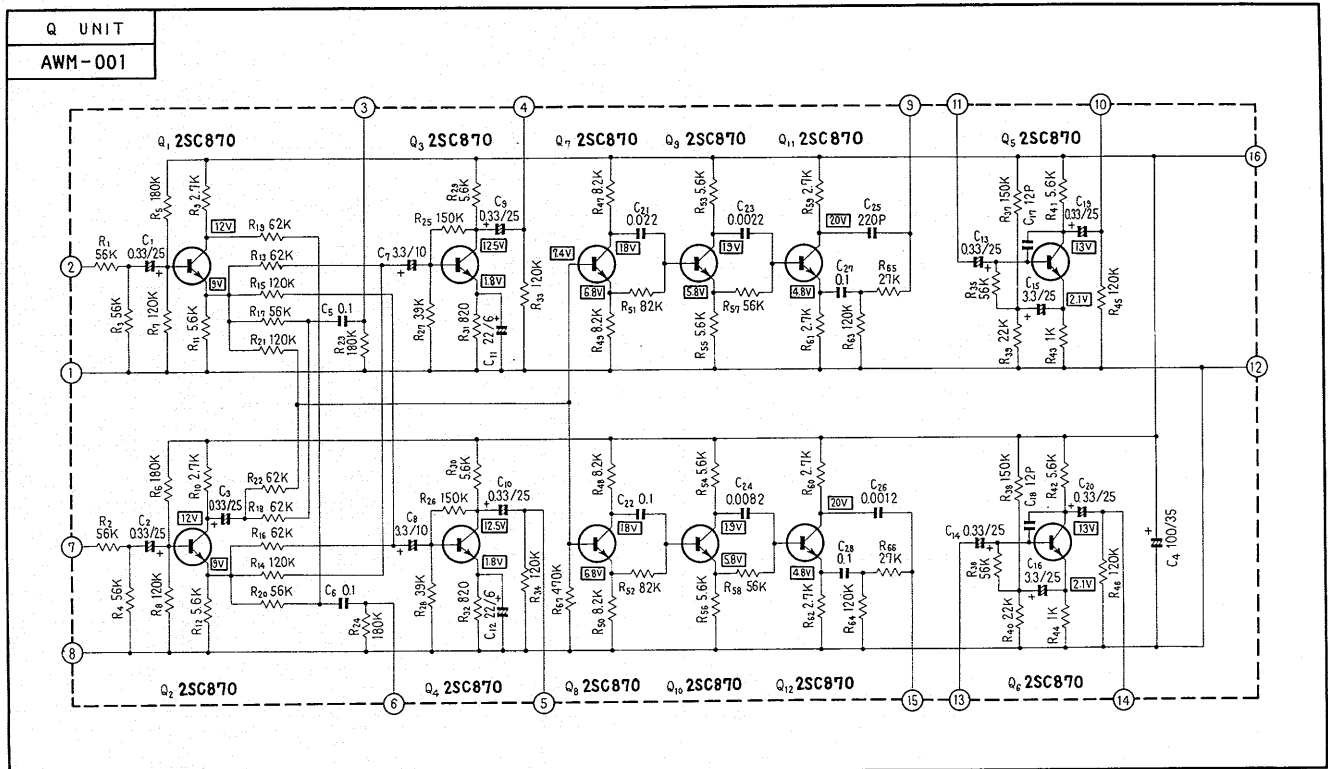
QC-800

UNIT	ROUND LABEL	FVW					
FM FRONT END							
FM IF UNIT							
MPX UNIT							
AM TUNER UNIT							
Q UNIT	AWM-001						
HEAD AMP UNIT	AWF-002						
CONTROL AMP UNIT	AWG-002						
MAIN AMP UNIT							
POWER SUPPLY UNIT	AWR-003						
SWITCH UNIT	AWS-001						
SWITCH UNIT	AWS-002						
SWITCH UNIT							
HEAD PHONE UNIT	AWK-001						
CONTROL UNIT							
MIC AMP UNIT							
REVERBE AMP UNIT							
REVERBE UNIT							

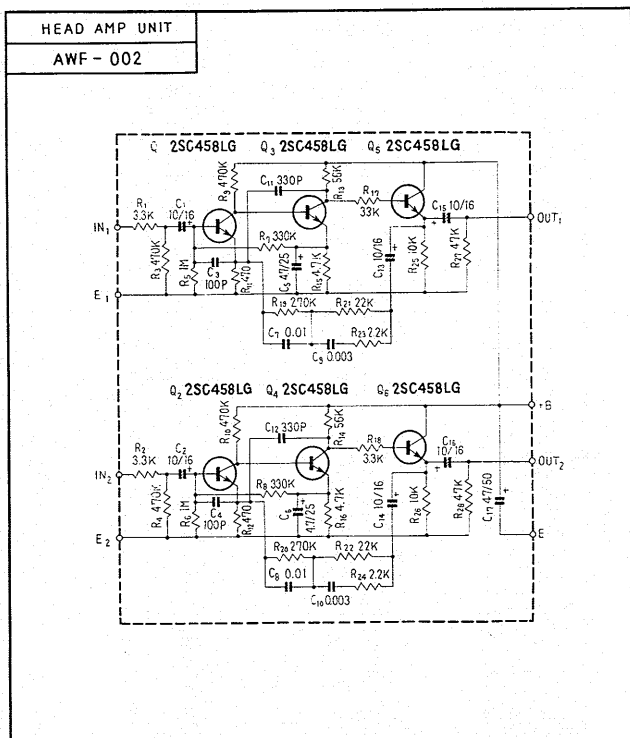




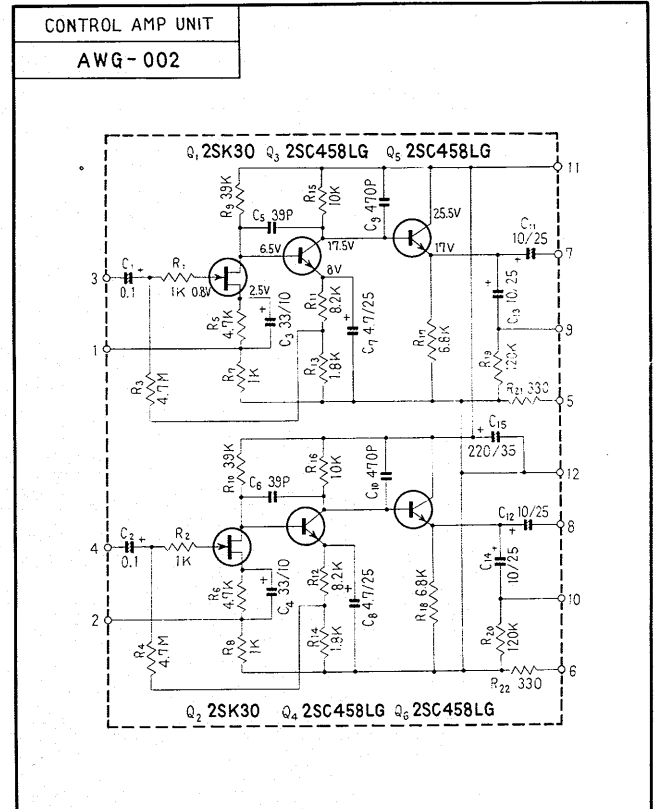
Q UNIT



HEAD AMP UNIT



CONTROL AMP UNIT



The schematic diagram of the AWS-001 circuit is enclosed in a dashed rectangular box. It features three main functional sections: a Low Filter (S₃), a High Filter (S₂), and an Output (S₁).

- Low Filter (S₃):** This section is divided into two parallel filter stages. The left stage consists of a series combination of capacitor C₈ (0.0033) and resistor R₄ (1M), followed by a shunt combination of resistor R₃ (1M) and capacitor C₇ (0.0033). The right stage consists of a series combination of capacitor C₆ (0.0033) and resistor R₂ (1M), followed by a shunt combination of resistor R₁ (1M) and capacitor C₅ (0.0033). Each stage has two switchable paths indicated by circles.
- High Filter (S₂):** This section also has two parallel filter stages. The left stage consists of a series combination of capacitor C₄ (0.0022) and capacitor C₃ (0.0022), followed by a shunt capacitor C₃ (0.0022). The right stage consists of a series combination of capacitor C₂ (0.0022) and capacitor C₁ (0.0022), followed by a shunt capacitor C₁ (0.0022). Each stage has two switchable paths indicated by circles.
- Output (S₁):** This section contains two switchable paths, each with two switchable points indicated by circles.

The circuit is connected to a 13-pin D-sub connector. Pins 19, 20, 21, 22, and 23 are at the top, while pins 25, 24, 18, 17, 16, 15, and 14 are at the bottom. Pins 1 through 13 are on the right side. The connections are as follows:

- Pins 19, 20, 21, 22, and 23 connect to the input of the Low Filter (S₃).
- Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, and 13 connect to the output of the Low Filter (S₃).
- Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, and 13 connect to the input of the High Filter (S₂).
- Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, and 13 connect to the output of the High Filter (S₂).
- Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, and 13 connect to the output of the Output (S₁).

The diagram illustrates the internal wiring of a 40-pin DIP package. The components and their connections are as follows:

- TAPE MON 1 S4:** Connected to pins 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32. It includes resistors R_4 (1K) and R_2 (1K).
- TAPE MON 2 S3:** Connected to pins 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32. It includes resistors R_3 (1K) and R_1 (1K).
- QUADRA-LIZER S2:** Connected to pins 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32. It includes resistors R_7 (39K), R_8 (47K), R_9 (39K), R_6 (47K), R_5 (47K), and R_4 (39K).
- MUTING S1:** Connected to pins 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32. It includes resistors R_7 (39K), R_8 (47K), R_9 (39K), R_6 (47K), R_5 (47K), and R_4 (39K).

The diagram also shows a series of switches (S1, S2, S3, S4) and their connections to the pins. The components are labeled with their values: R_1 1K, R_2 1K, R_3 1K, R_4 1K, R_5 47K, R_6 47K, R_7 39K, R_8 47K, R_9 39K.

HEADPHONE AMP UNIT

AWK - 001

The diagram illustrates a two-stage audio amplifier circuit, labeled "HEADPHONE AMP UNIT" and "AWK - 001". The circuit is enclosed in a dashed rectangular box and features 8 numbered terminals (1-8) for input and output.

Transistors: Two 2SC373 transistors are used, labeled Q_1 and Q_2 .

Stage 1 (Top):

- Input terminal 3 is connected to a network of resistors R_1 (470K) and R_9 (1K) in series, followed by a capacitor C_1 (0.1) in parallel with R_9 .
- The base of Q_1 is connected to the junction of R_9 and C_1 .
- A resistor R_5 (180K) is connected between the base and the positive supply rail.
- A resistor R_3 (100K) is connected between the emitter and ground.
- The collector of Q_1 is connected to a transformer T_1 (00000/00000) and a resistor R_7 (4.7K) in parallel.
- A capacitor C_3 (47/25) is connected between the collector and the positive supply rail.
- Output terminals 1 and 5 are connected to the secondary of transformer T_1 .

Stage 2 (Bottom):

- Input terminal 4 is connected to a network of resistors R_2 (470K) and R_{10} (1K) in series, followed by a capacitor C_2 (0.1) in parallel with R_{10} .
- The base of Q_2 is connected to the junction of R_{10} and C_2 .
- A resistor R_6 (180K) is connected between the base and the positive supply rail.
- A resistor R_4 (100K) is connected between the emitter and ground.
- The collector of Q_2 is connected to a transformer T_2 (00000/00000) and a resistor R_8 (4.7K) in parallel.
- A capacitor C_4 (47/25) is connected between the collector and the positive supply rail.
- Output terminals 2 and 6 are connected to the secondary of transformer T_2 .

Power and Grounding:

- Terminal 7 is the positive supply rail.
- Terminal 8 is the common ground.