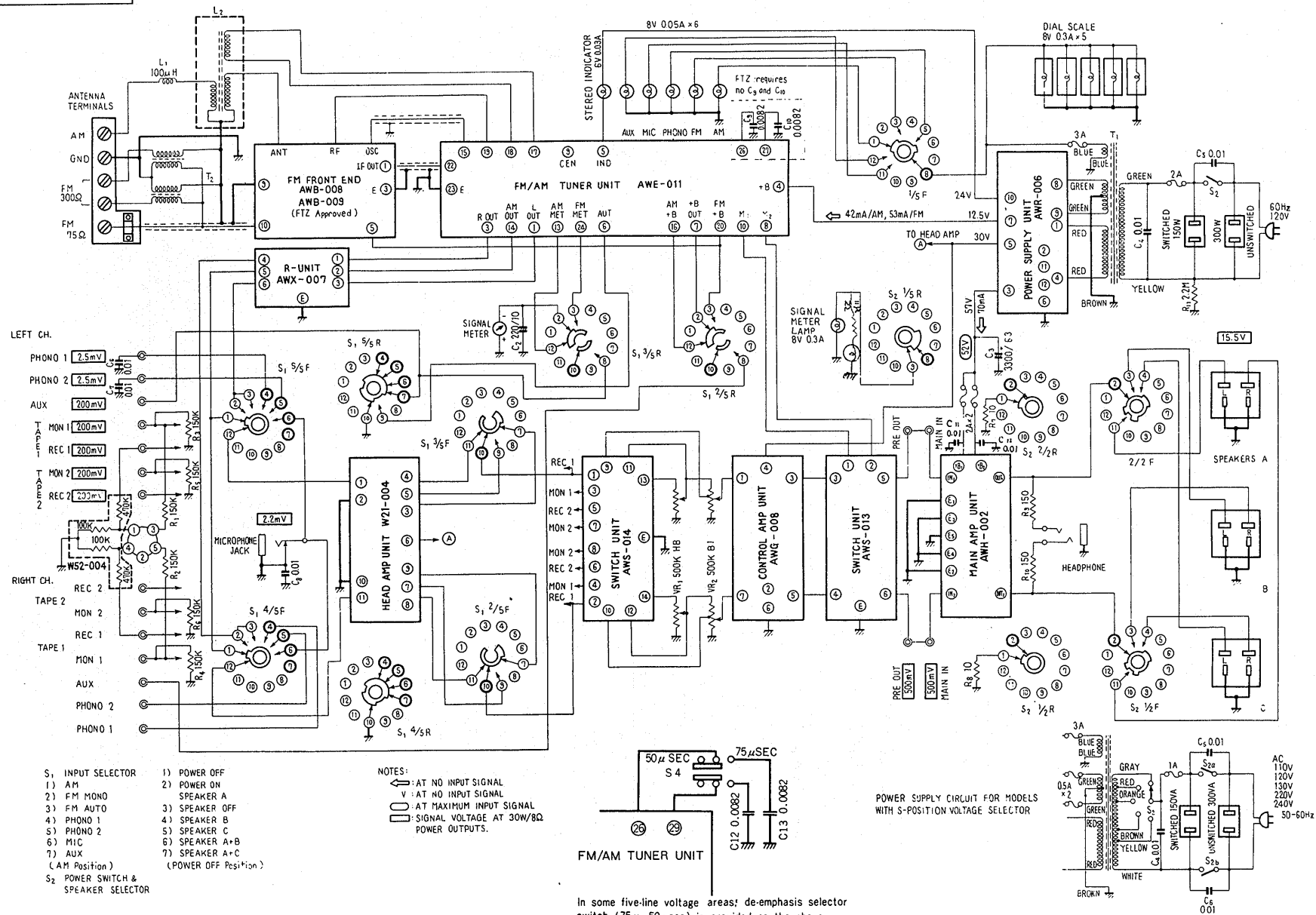


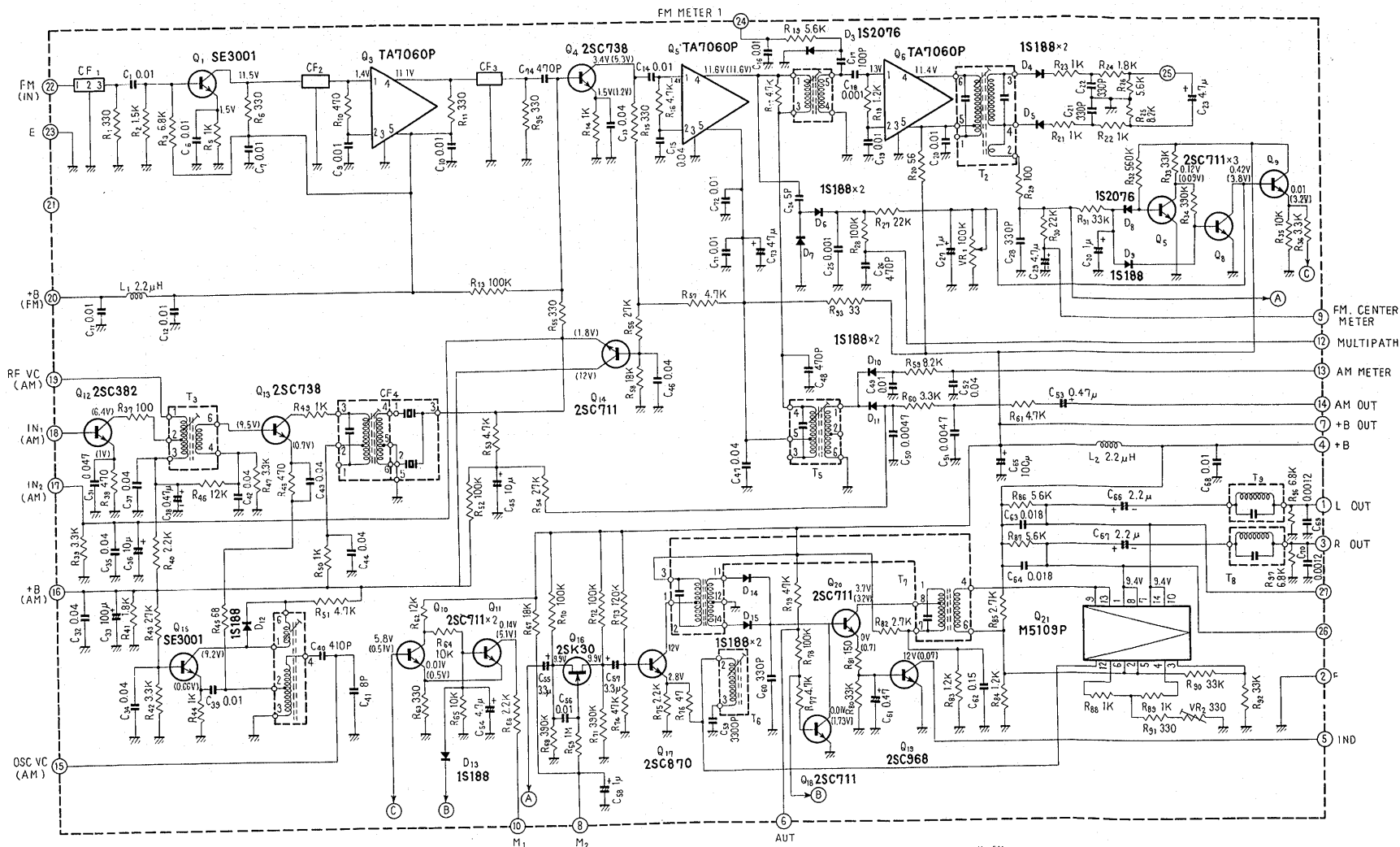
AM/FM STEREO RECEIVER

SX-626

UNIT \ ROUND LABEL	FVZW	KW				
FM FRONT END	AWB-009	AWB-008				
FM/AM TUNER UNIT	AWE-011	AWE-011				
MPX UNIT						
AM TUNER UNIT						
MUTING UNIT						
HEAD AMP UNIT	W21-004	W21-004				
CONTROL AMP UNIT	AWG-008	AWG-008				
MAIN AMP UNIT	AWH-002	AWH-002				
POWER SUPPLY UNIT	AWR-006	AWR-006				
SWITCH UNIT	AWS-013	AWS-013				
SWITCH UNIT	AWS-014	AWS-014				
R UNIT	AWX-007	AWX-007				
TONE COLOR UNIT						
CONTROL UNIT						
MIC AMP UNIT						
REVERBE AMP UNIT						
REVERBE UNIT						







V: FM
 (V): AM
 (V): FM STEREO

FM-FRONT END

AWB-008
AWB-009 (FTZ APPROVED)

The schematic diagram illustrates the FM-FRONT END circuit, which is divided into three main sections by dashed lines: the input/oscillator section, the intermediate frequency (IF) amplifier section, and the output section.

Input/Oscillator Section: The circuit starts with an ANT (Antenna) input at 5Ω and an AGC (Automatic Gain Control) input at 8Ω. The signal is coupled through a matching network consisting of L₁, CV₁, and TC₁ to the base of the first transistor, Q₁ (3SK30). A 75Ω resistor is connected to the antenna input. The base of Q₁ is also connected to a 100K resistor (R₁) and a 1MΩ resistor (R₃) in parallel, which is then connected to ground through a 0.005μF capacitor (C₄). The emitter of Q₁ is connected to ground through a 100Ω resistor (R₂) and a 0.005μF capacitor (C₃). The collector of Q₁ is connected to a 47Ω resistor (R₄) and a 22pF capacitor (C₂), which is then connected to the base of the second transistor, Q₂ (2SC535).

IF Amplifier Section: The signal is coupled through a matching network consisting of L₂, CV₂, and TC₂ to the base of Q₂. The base of Q₂ is also connected to a 100Ω resistor (R₅) and a 0.005μF capacitor (C₇) in parallel, which is then connected to ground through a 0.01μF capacitor (C₈). The emitter of Q₂ is connected to ground through a 100Ω resistor (R₆) and a 0.005μF capacitor (C₉). The collector of Q₂ is connected to a 22K resistor (R₇) and a 100pF capacitor (C₆) in parallel, which is then connected to the base of the third transistor, Q₃ (SE3001).

Output Section: The signal is coupled through a matching network consisting of L₃, CV₃, and TC₃ to the base of Q₃. The base of Q₃ is also connected to a 100Ω resistor (R₈) and a 0.005μF capacitor (C₁₀) in parallel, which is then connected to ground through a 0.005μF capacitor (C₁₁). The emitter of Q₃ is connected to ground through a 100Ω resistor (R₉) and a 0.005μF capacitor (C₁₂). The collector of Q₃ is connected to a 10K resistor (R₁₀) and a 33pF capacitor (C₁₃) in parallel, which is then connected to the IF OUT (Intermediate Frequency Output) at 10Ω. The output is also connected to a 0.005μF capacitor (C₁₄) and a 100pF capacitor (C₁₅) in parallel, which is then connected to ground through a 0.005μF capacitor (C₁₆).

The circuit is powered by a +B (Battery) supply at 5V, which is connected to the emitter of Q₂ and the emitter of Q₃ through a 0.005μF capacitor (C₁₇). The output of the circuit is connected to a 7Ω load through a 0.005μF capacitor (C₁₈).

HEAD AMP UNIT

W21 - 004

2

1

3

5

4

6

8

11

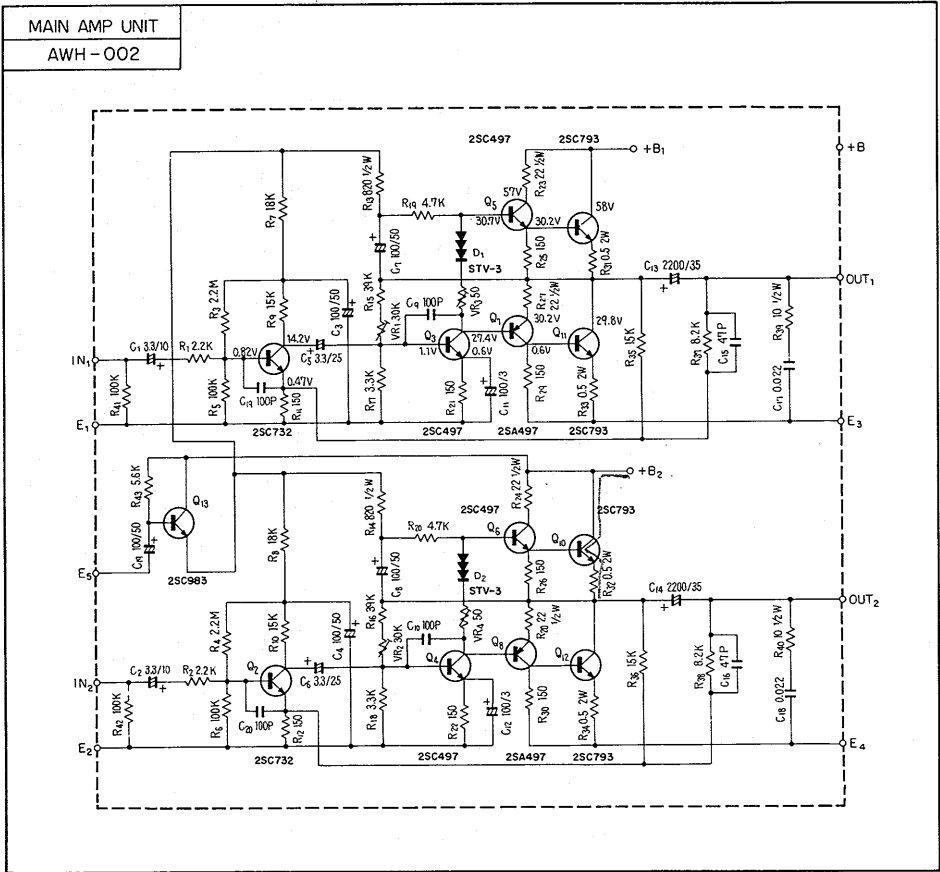
100

7

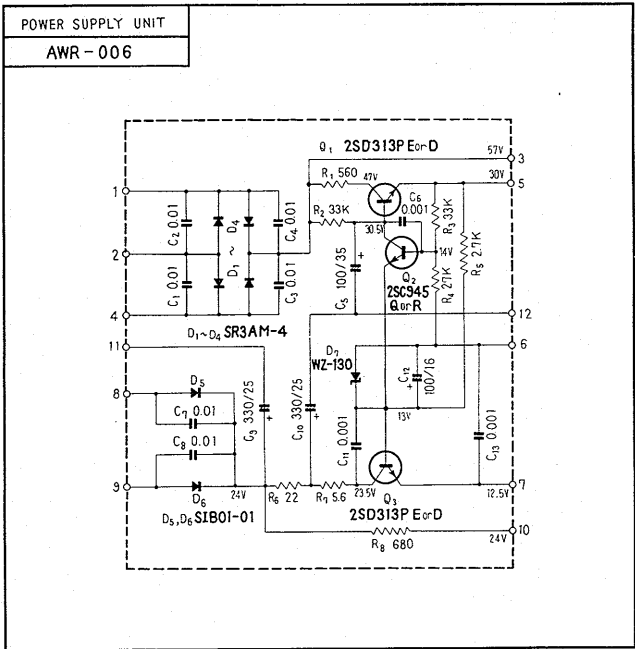
9

[illegible]

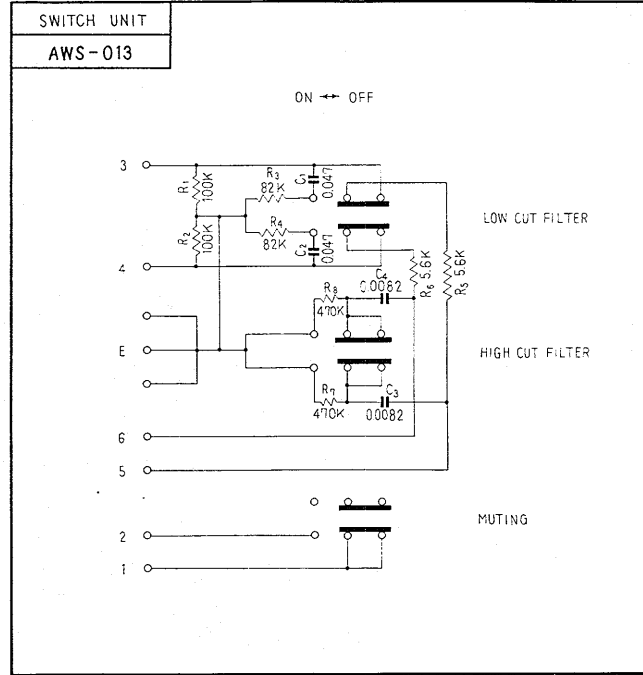
MAIN AMP UNIT



POWER SUPPLY UNIT



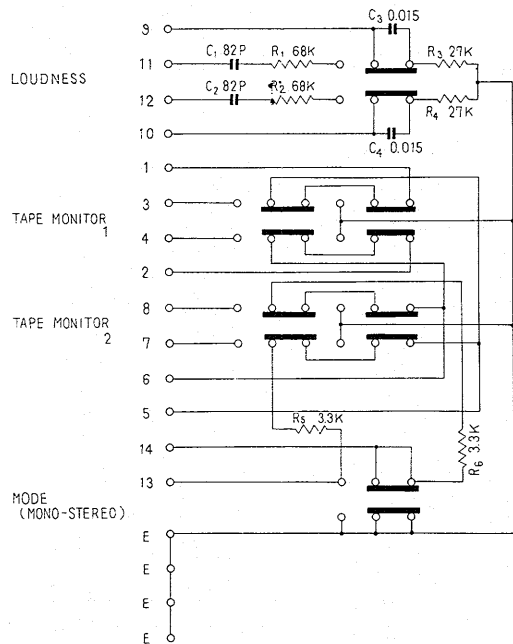
SWITCH UNIT



SWITCH UNIT

SWITCH UNIT

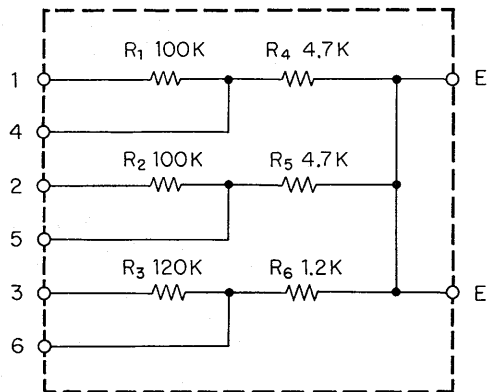
AWS-014



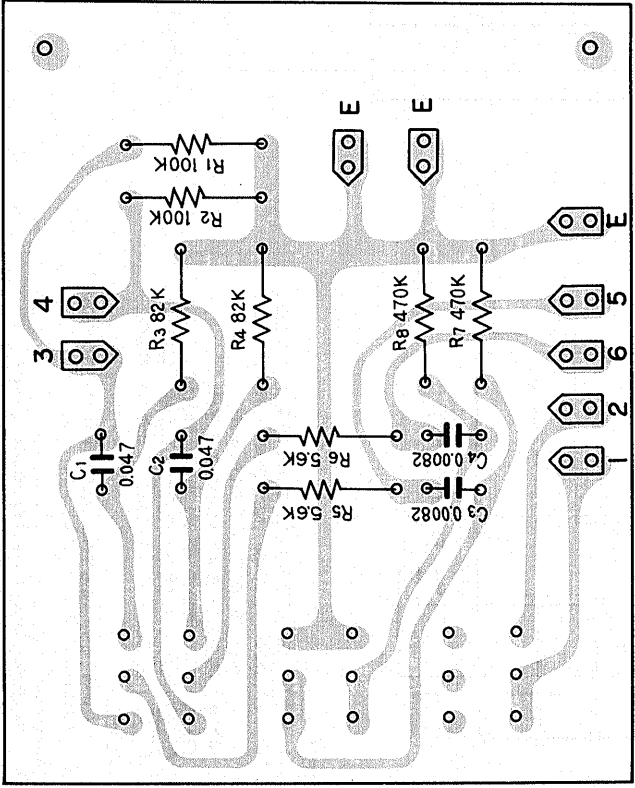
R UNIT

R - UNIT

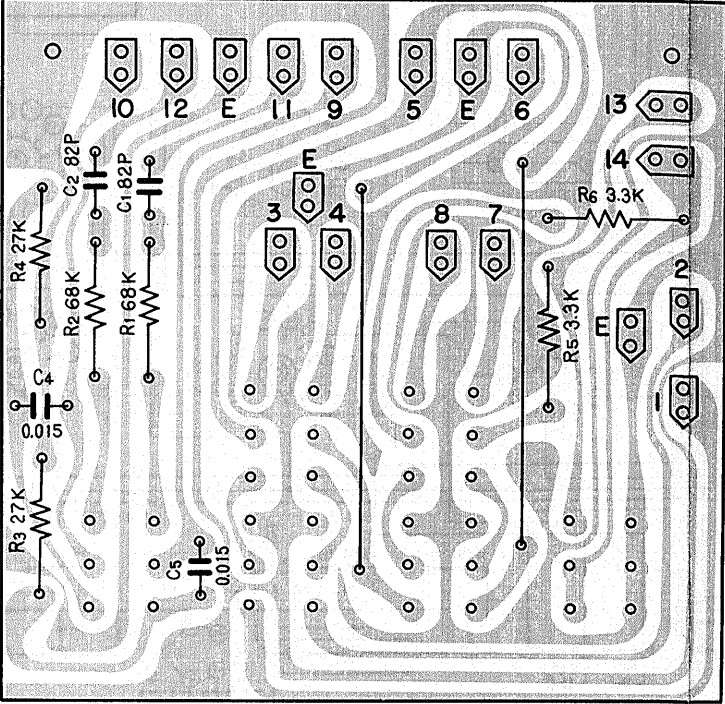
AWX - 007



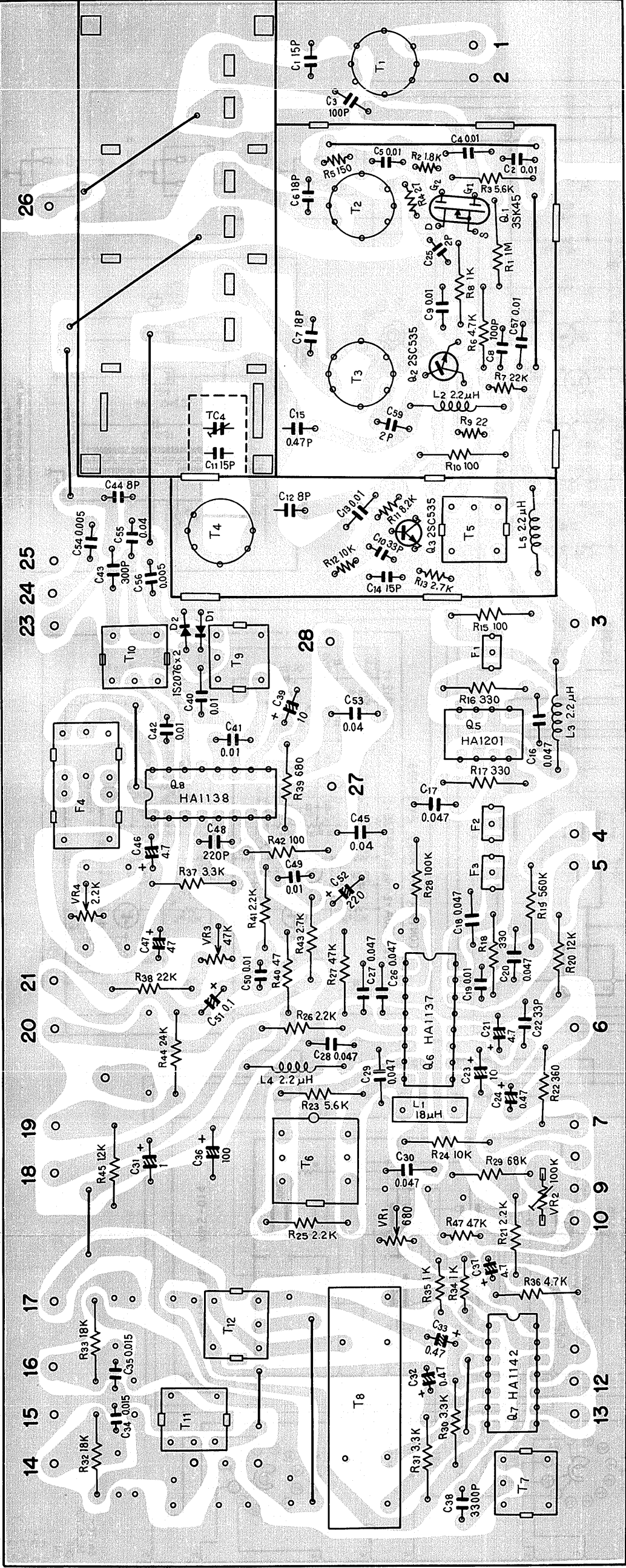
Switch circuit ass'y AWS-013



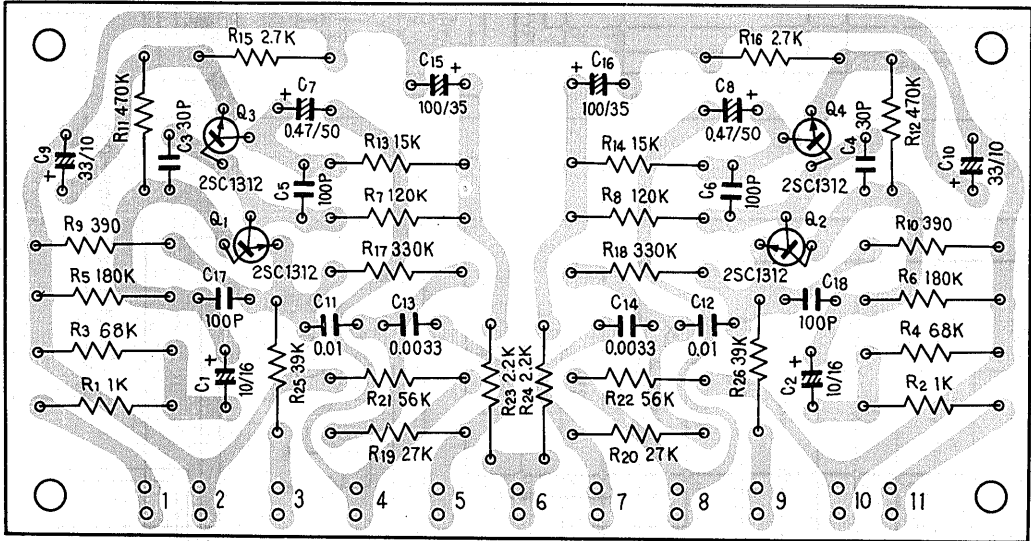
Switch circuit ass'y AWS-014



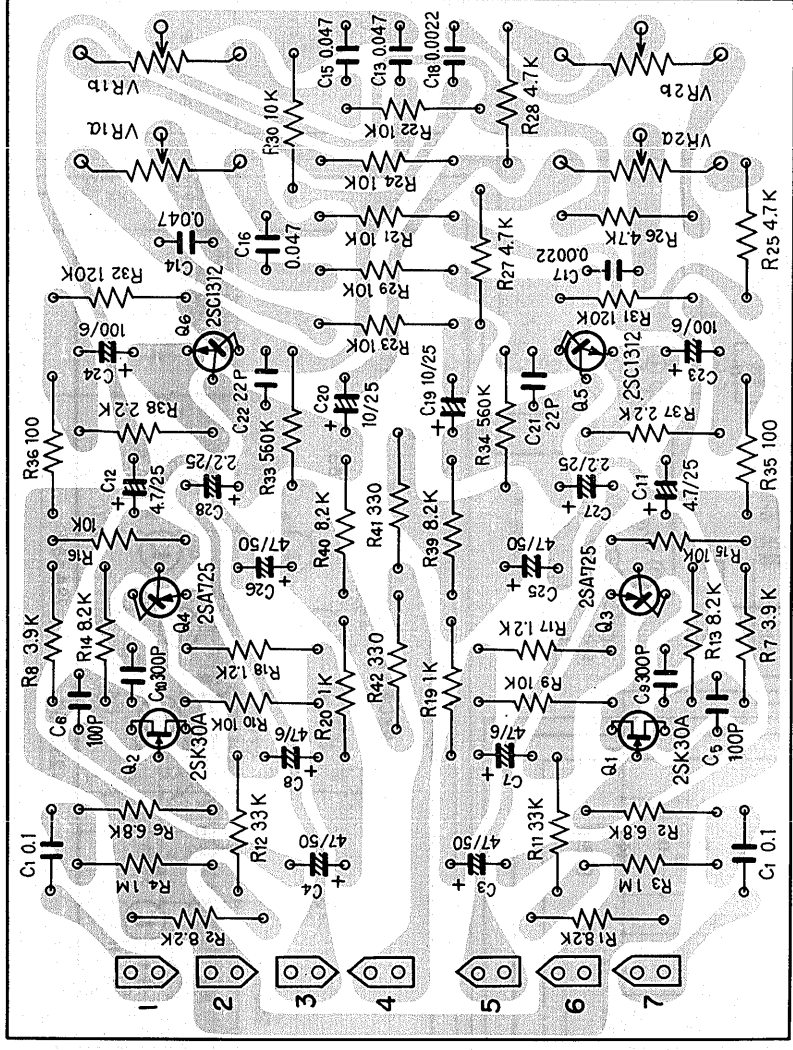
Tuner ass'y AWE-031



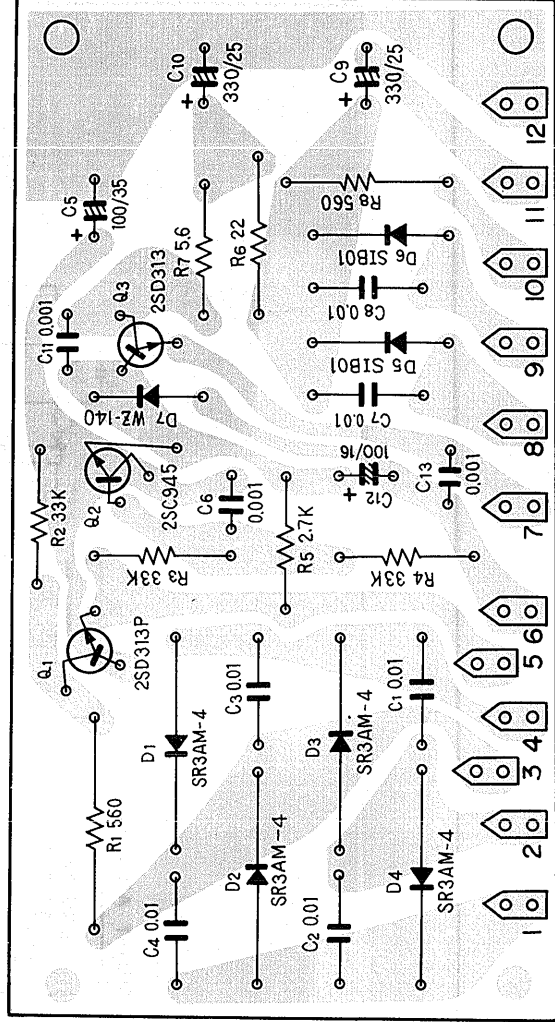
Equalizer amp ass'y W21-004



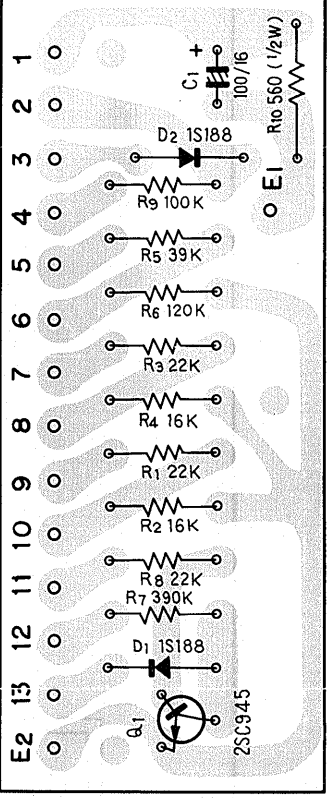
Control amp ass'y AWG-008



Power supply ass'y AWR-029



CR, meter drive ass'y AWM-050



Power amp ass'y AWH-002

