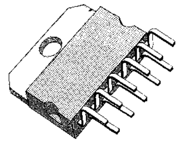
SGS®

TDA2009

10 + 10W HIGH QUALITY STEREO AMPLIFIER

The TDA2009 is class AB dual Hi-Fi Audio power amplifier assembled in Multiwatt® package, specially designed for high quality stereo application as Hi-Fi and music centers. Its main features are:

- High output power (10 + 10W min. @ d = 0.5%)
- High current capability (up to 3.5A)
- Thermal overload protection
- Space and cost saving: very low number of external components and simple mounting thanks to the Multiwatt® package.



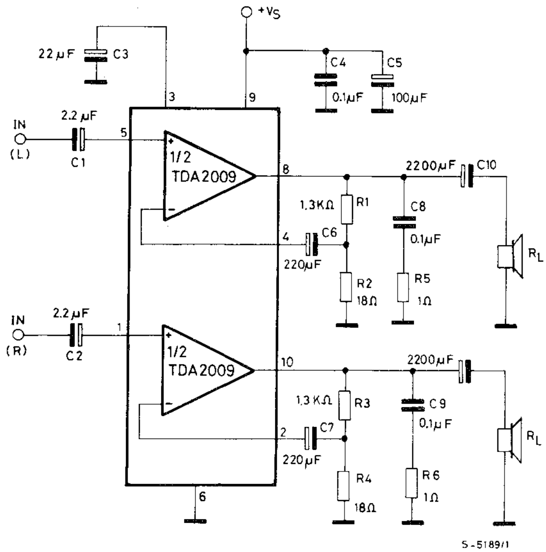
Multiwatt-11

ORDERING NUMBER: TDA2009

ABSOLUTE MAXIMUM RATINGS

V_s	Supply voltage	28	V
I_o	Output peak current (repetitive f ⩾ 20Hz)	3.5	A
I_o	Output peak current (non repetitive, t = 100μs)	4.5	A
P_{tot}	Power dissipation at T_{case} = 90°C	20	W
T_{stg}, T_j	Storage and junction temperature	-40 to 150	°C

TEST CIRCUIT



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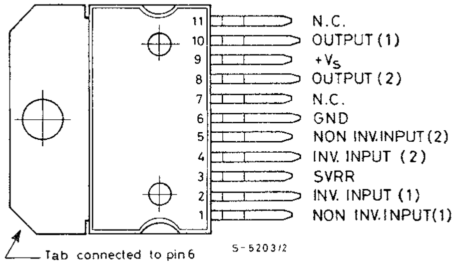
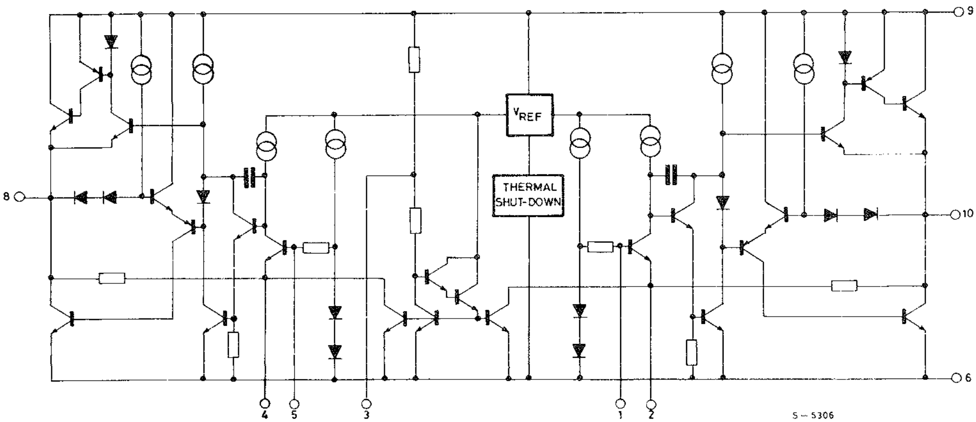
CONNECTION DIAGRAM
(top view)



SCHEMATIC DIAGRAM



THERMAL DATA

$R_{th\ j\text{-}case}$	Thermal resistance junction-case	max	3	°C/W

Fig. 1 - Test and application circuit (G_v = 36 dB)

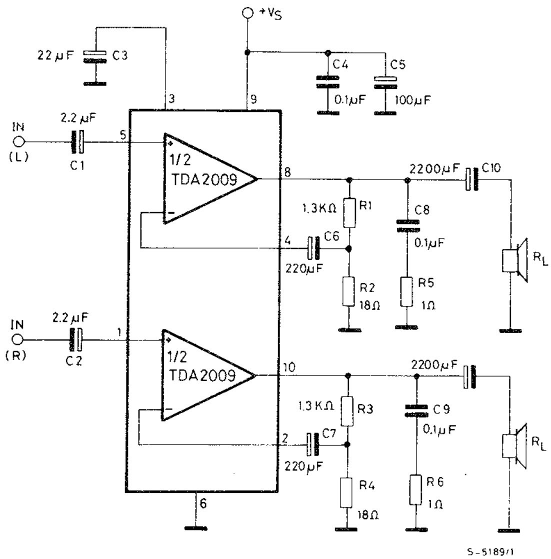
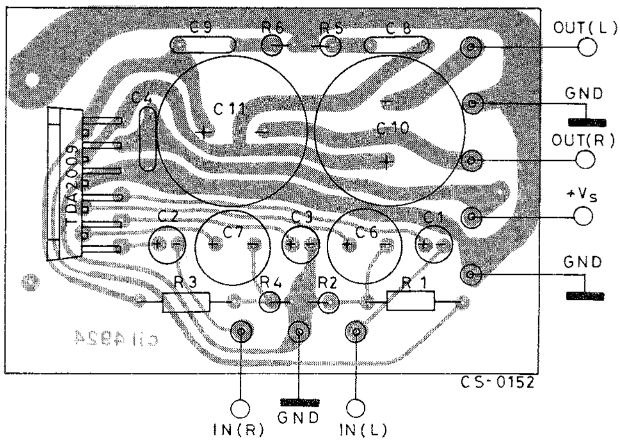


Fig. 2 - P.C. board and components layout of the circuit of fig. 1 (1 : 1 scale)



ELECTRICAL CHARACTERISTICS (Refer to the **stereo** application circuit, T_{amb} = 25°C, G_v = 36 dB, unless otherwise specified)

	Parameters	Test conditions	Min.	Typ.	Max.	Unit
V_s	Supply voltage		8		28	V
V_o	Quiescent output voltage	V_s = 23V		11		V
I_d	Total quiescent drain current	V_s = 23V		80	120	mA
P_o	Output power (each channel)	f = 50 Hz to 16 KHz d = 0.5% V_s = 23V R_L = 4 Ω R_L = 8 Ω V_s = 18V R_L = 4 Ω R_L = 8 Ω	 10 5.5 	 11 7 6.5 4		 W W W W
d	Distortion (each channel)	f = 1 KHz V_s = 23V R_L = 4 Ω P_o = 100 mW to 8W V_s = 23V R_L = 8 Ω P_o = 100 mW to 3W		0.1 0.05		%
CT	Cross talk (°°°)	R_L = ∞ f = 1 KHz		60		dB
		R_g = 10 KΩ f = 10 KHz		50		dB
V_i	Input saturation voltage (rms)		300			mV
R_i	Input resistance	f = 1 KHz non inverting input	70	200		KΩ
		inverting input		10		KΩ
f_L	Low frequency roll off (-3 dB)	R_L = 4Ω		20		Hz
f_H	High frequency roll off (-3 dB)			80		KHz
G_v	Voltage gain (open loop)	f = 1 KHz		85		dB
G_v	Voltage gain (closed loop)	f = 1 KHz	35.5	36	36.5	dB
ΔG_v	Closed loop gain matching			0.5		dB
e_N	Total input noise voltage	R_g = 10 KΩ (°)		1.5		μV
		R_g = 10 KΩ (°°)		2.5	8	μV
SVR	Supply voltage rejection (each channel)	R_g = 10 KΩ f_{ripple} = 100 Hz V_{ripple} = 0.5V		55		dB
T_J	Thermal shut-down junction temperature			145		°C

(°) Curve A. (°°) 22 Hz to 22 KHz. (°°°) Optimized test box.

Fig. 3 - Output power vs. supply voltage

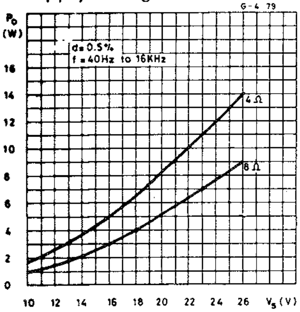


Fig. 4 - Output power vs. supply voltage

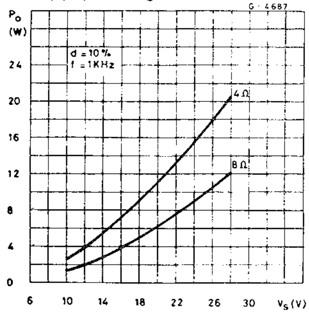


Fig. 5 - Distortion vs. output power

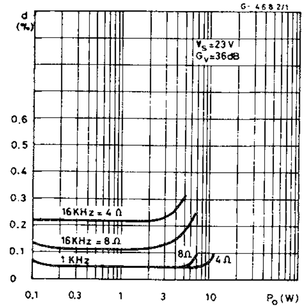


Fig. 6 - Distortion vs. frequency

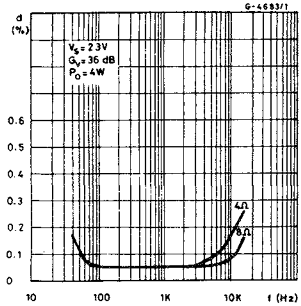


Fig. 7 - Quiescent current vs. supply voltage

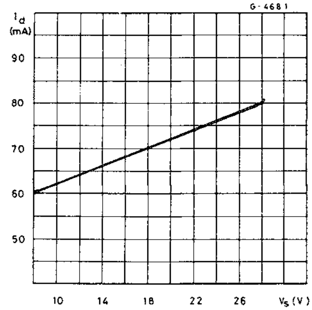


Fig. 8 - Supply voltage rejection vs. value of capacitor C3

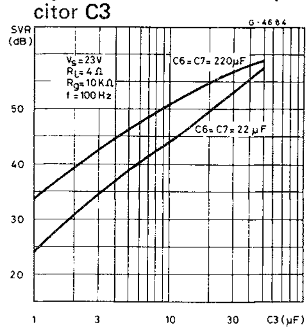


Fig. 9 - Supply voltage rejection vs. frequency

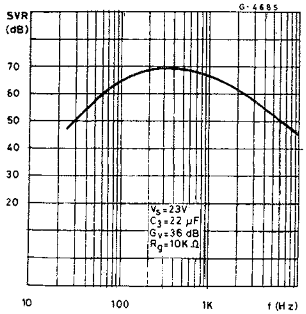


Fig. 10 - Total power dissipation an efficiency vs. output power

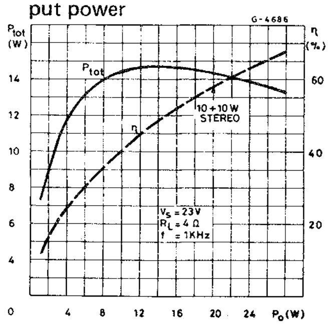
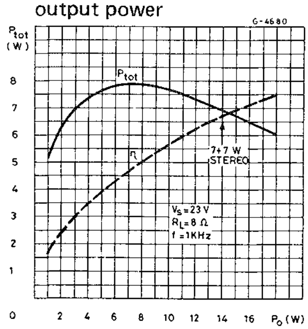


Fig. 11 - Total power dissipation and efficiency vs. output power



APPLICATION INFORMATION

Fig. 12 - Simple short-circuit protection

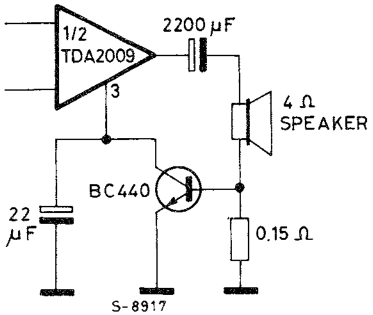


Fig. 13 - Example of muting circuit

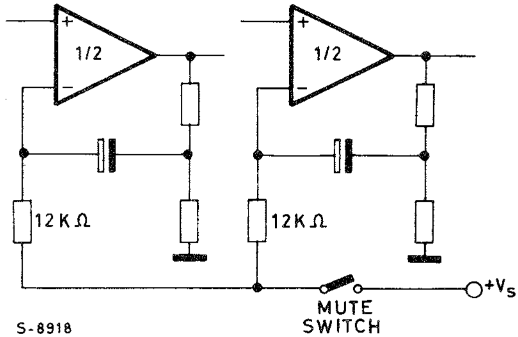


Fig. 14 - 10 + 10W stereo amplifier with tone balance and loudness control

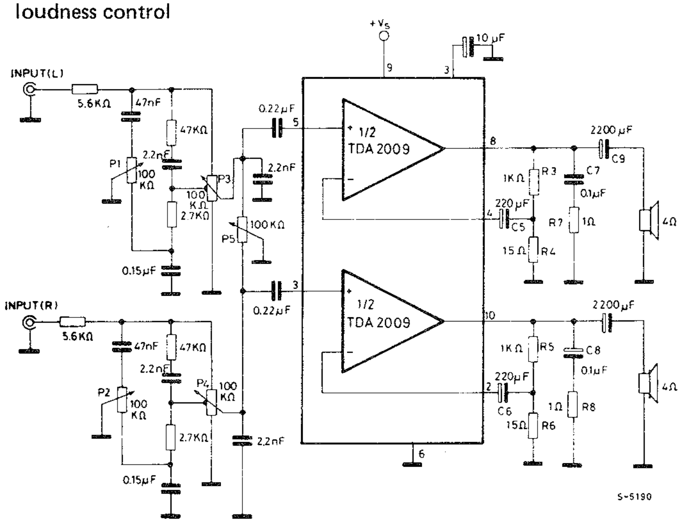
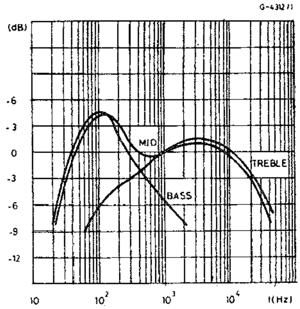


Fig. 15 - Tone control response (circuit of fig. 14)



APPLICATION INFORMATION (continued)

Fig. 16 - High quality 20 + 20W two way amplifier for stereo music center (one channel only).

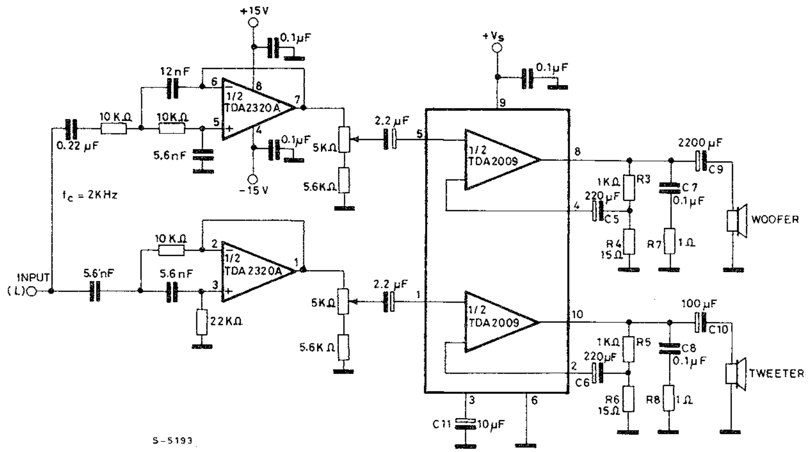


Fig. 17 - 18W bridge amplifier (d = 0.5%, G_v = 40dB)

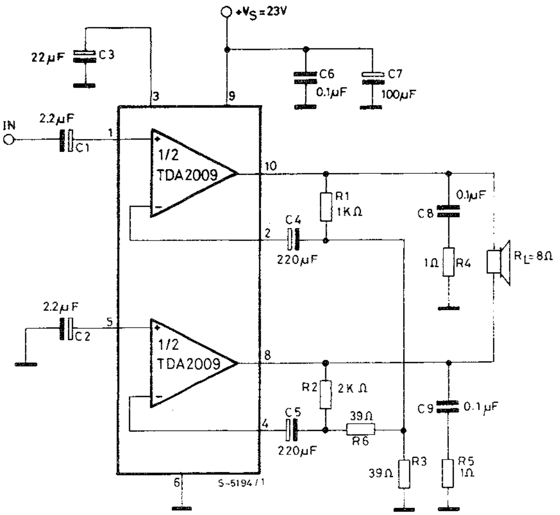
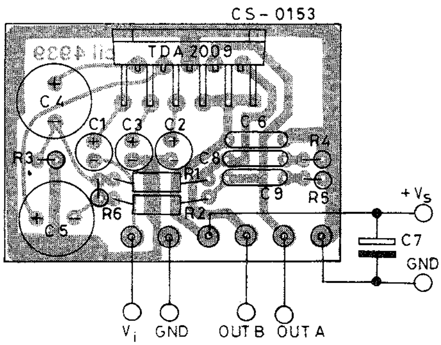


Fig. 18 - P.C. board and components layout of the circuit of fig. 17 (1 : 1 scale)



APPLICATION SUGGESTION

The recommended values of the components are those shown on application circuit of fig. 1 . Different values can be used; the following table can help the designer.

Component	Recomm. value	Purpose	Larger than	Smaller than
R1 and R3	1.2 KΩ	Close loop gain setting	Increase of gain	Decrease of gain
R2 and R4	18 Ω		Decrease of gain	Increase of gain
R5 and R6	1 Ω	Frequency stability	Danger of oscillation at high frequency with inductive load	
C1 and C2	2.2 μF	Input DC decoupling	High turn-on delay	High turn-on pop Higher low frequency cutoff. Increase of noise
C3	22 μF	Ripple rejection	Better SVR. Increase of the switch-on time	Degradation of SVR.
C6 and C7	220 μF	Feedback Input DC decoupling.		
C8 and C9	0.1 μF	Frequency stability.		Danger of oscillation.
C10 and C11	1000 μF to 2200 μF	Output DC decoupling.		Higher low-frequency cut-off.

MOUNTING INSTRUCTIONS

The power dissipated in the circuit must be removed by adding an external heatsink.
Thanks to the MULTIWATT® package attaching the heatsink is very simple, a screw or a compression spring (clip) being sufficient. Between the heatsink and the package it is better to insert a layer of silicon grease, to optimize the thermal contact; no electrical isolation is needed between the two surfaces.