

No.2508B

2SC3995

NPN Triple Diffused Planar Silicon Transistor

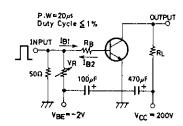
Ultrahigh-Definition Display Horizontal Deflection Output Applications

Features

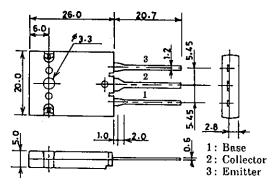
- · High speed ($t_f = 100 ns typ$).
- · High reliability (Adoption of HVP process).
- · High breakdown voltage ($V_{CBO} = 1500V$).
- · Adoption of MBIT process.

Absolute Maximum Ratings at Collector-to-Base Voltage Collector-to-Emitter Voltage Emitter-to-Base Voltage Collector Current	Ta = 25°C V_{CBO} V_{CEO} V_{EBO} I_{C}		1	500 800 6 12	unit V V V A	
Collector Current (Pulse) Collector Dissipation Junction Temperature Storage Temperature	I _{CP} P _C Tj Tstg	Tc = 25°C	-55 to +	30 180 150	A W °C °C	
Electrical Characteristics at Ta = 25°C			min	typ	max	unit
Collector Cutoff Current Collector Cutoff Current	I _{CBO}	$V_{CB} = 800 V, I_{E} = 0$ $V_{CE} = 1500 V, R_{BE} = 0$		V) P	10 1.0	μA mA
Collector Sustain Voltage Emitter Cutoff Current	$V_{\mathrm{CEO(sus)}}$ I_{EBO}	$I_C = 100 \text{ mA}, I_B = 0$ $V_{EB} = 4 \text{ V}, I_C = 0$	800		1.0	V
C-E Saturation Voltage	V _{CE(sat)}	$I_{C} = 10A, I_{B} = 2.5A$			1.0 5	mA V
B-E Saturation Voltage	V _{BE(sat)}	$I_C = 10A, I_B = 2.5A$			1.5	V
DC Current Gain	$h_{FE}(1)$	$V_{CE} = 5V, I_{C} = 1.0A$	8		30	
G	h _{FE} (2)	$V_{CE} = 5V, I_C = 10A$	4		8	
Storage Time	$t_{ m stg}$	$I_C = 8A$, $I_{B1} = 1.6A$, $I_{B2} = -3.2A$			3.0	μ S
Fall Time	t_f	$I_C = 8A$, $I_{B2} = 1.6A$, $I_{B2} = -3.2A$			0.2	μ S

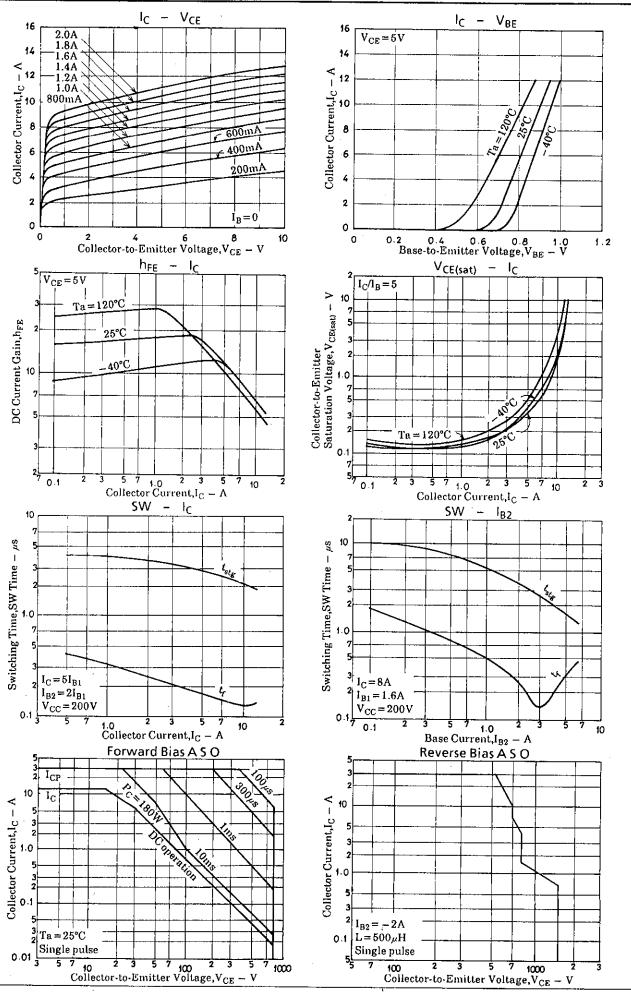
Switching Time Test Circuit

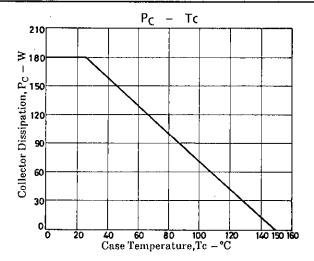


Package Dimensions 2048B (unit: mm)



SANYO: TO-3PBL





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